

FIG. 2

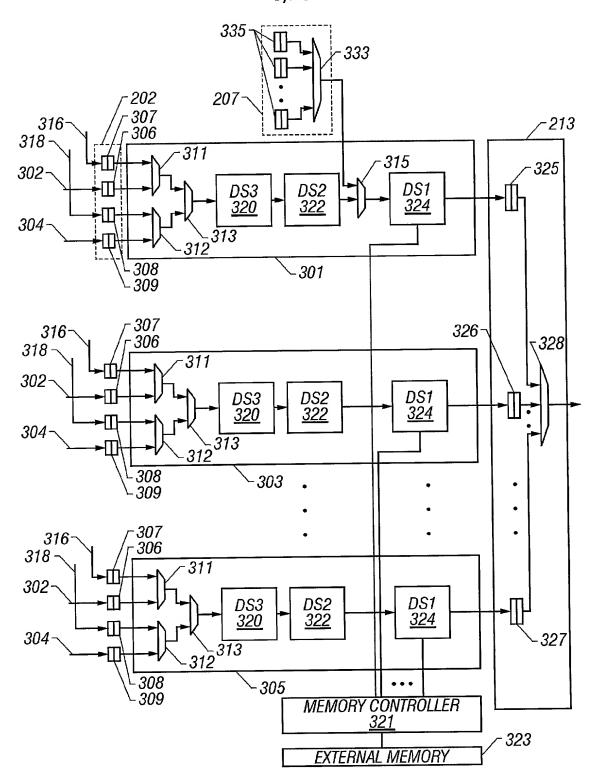
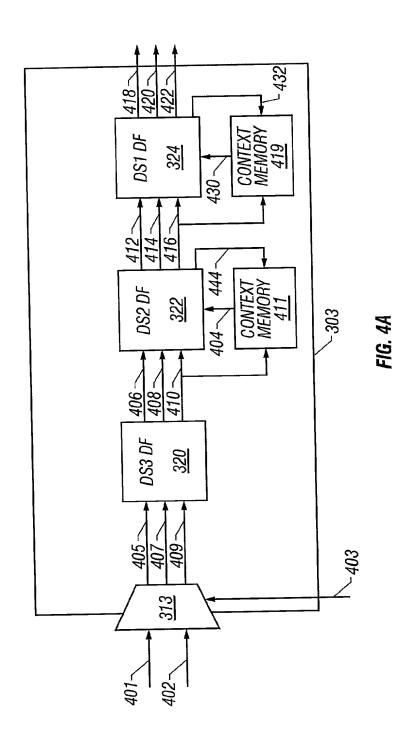
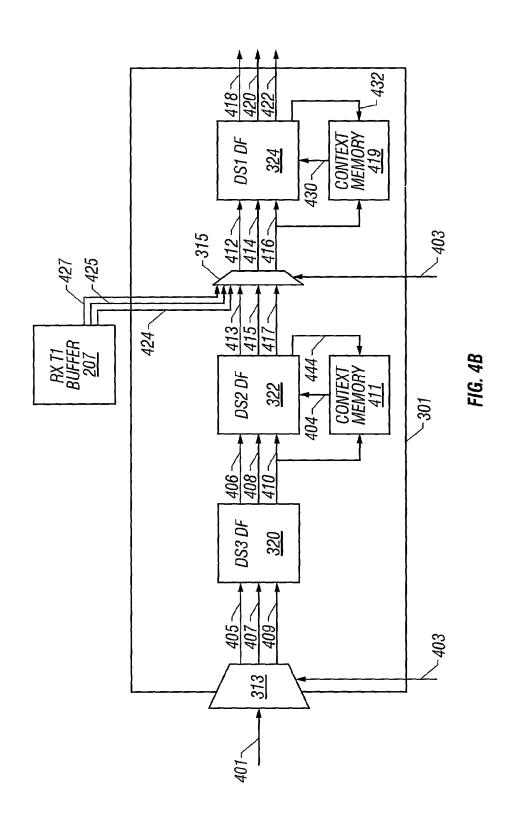


FIG. 3

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*5/23* 



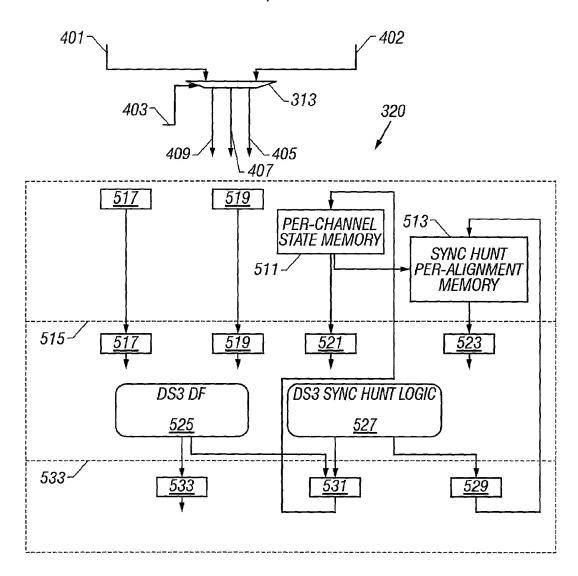


FIG. 5

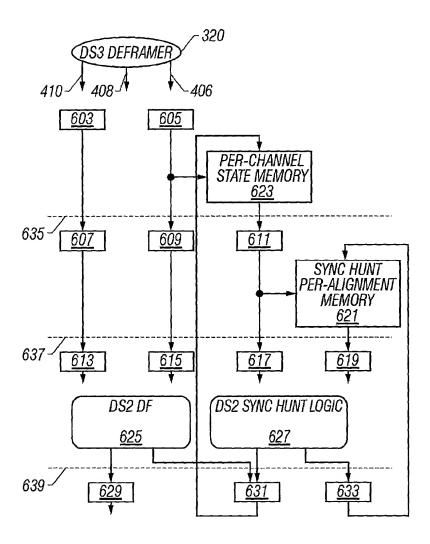


FIG. 6

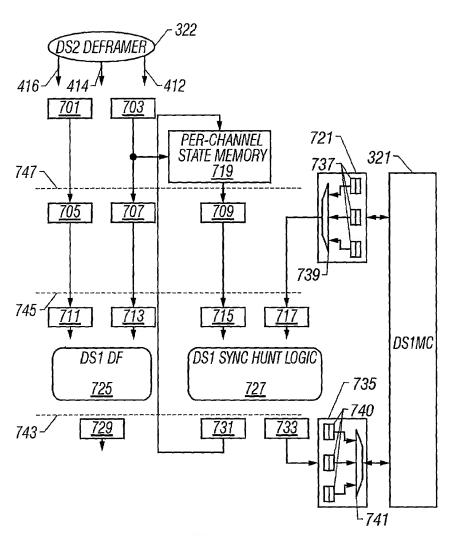


FIG. 7

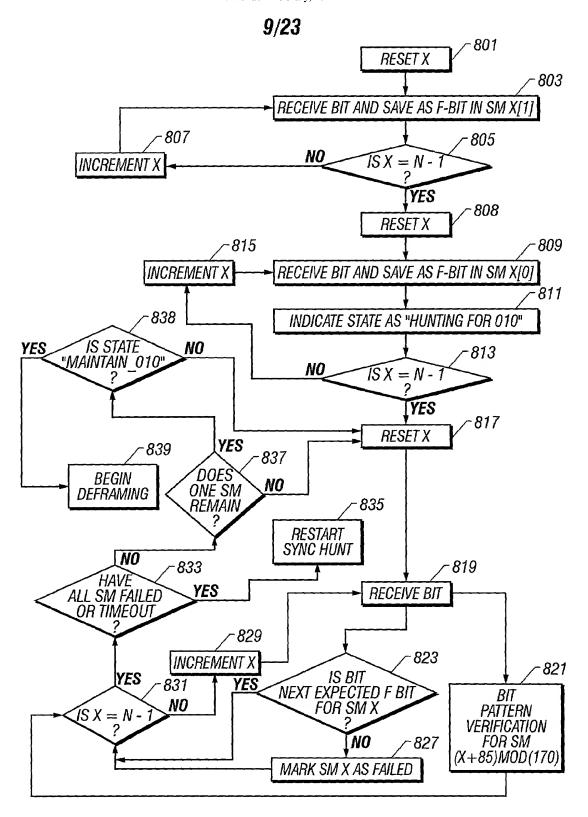


FIG. 8A

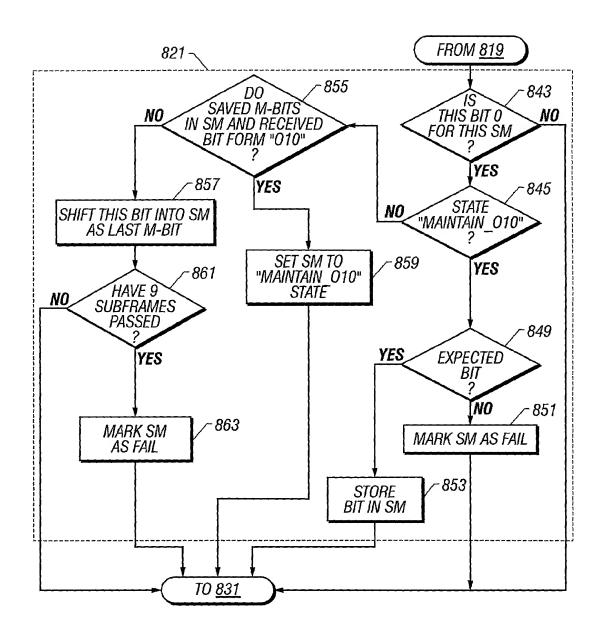


FIG. 8B

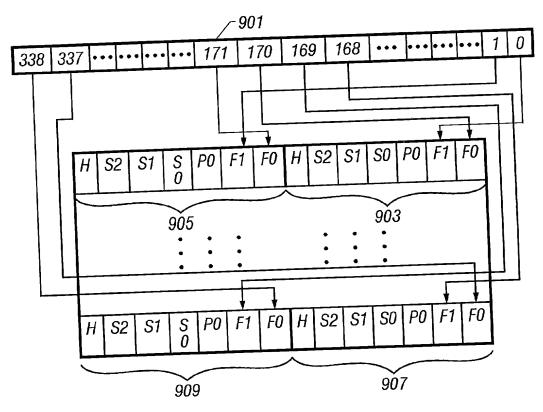


FIG. 9

SM 84	SM 169					
•	•					
•						
•						
SM 1	SM 86					
SM 0	SM 85					
<b>-</b>	+					

FIG. 10

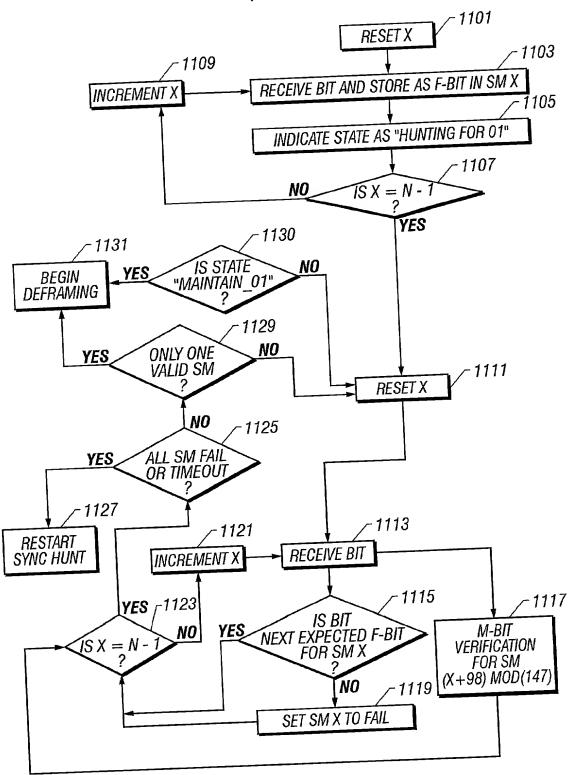


FIG. 11A

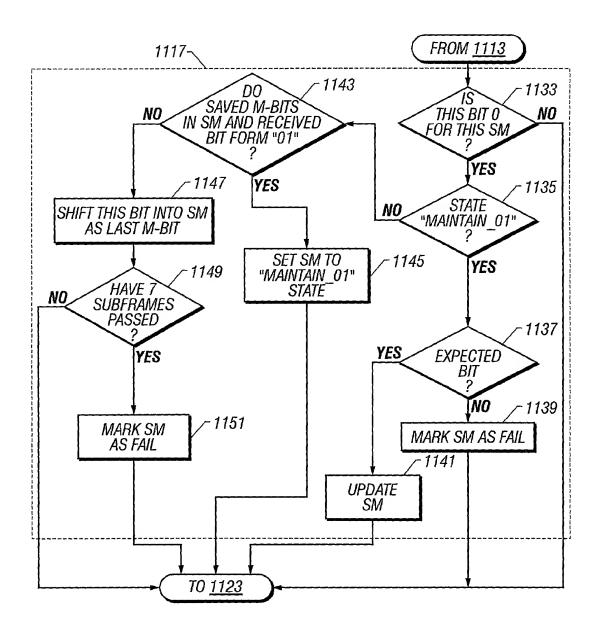


FIG. 11B

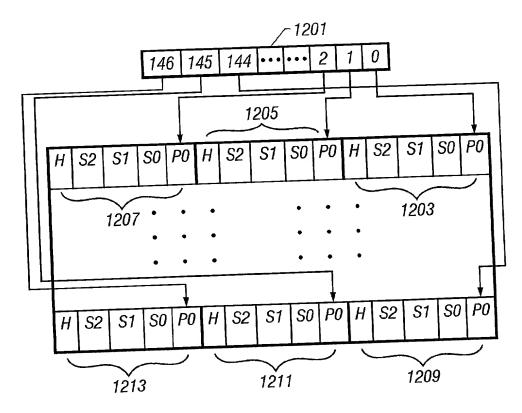


FIG. 12

	_	
SM 48	SM 98	SM 146
		_
•	•	•
•	•	•
•	•	•
•	•	•
		21100
SM 1	SM 50	SM 99
SM 0	SM 49	SM 98
	· · · · · · · · · · · · · · · · · · ·	1

FIG. 13

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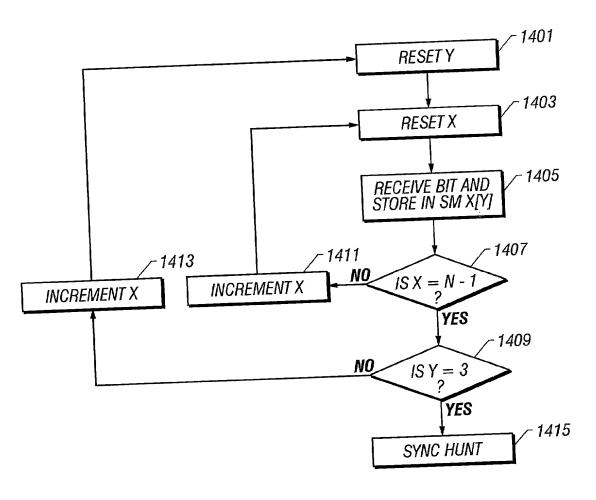


FIG. 14A

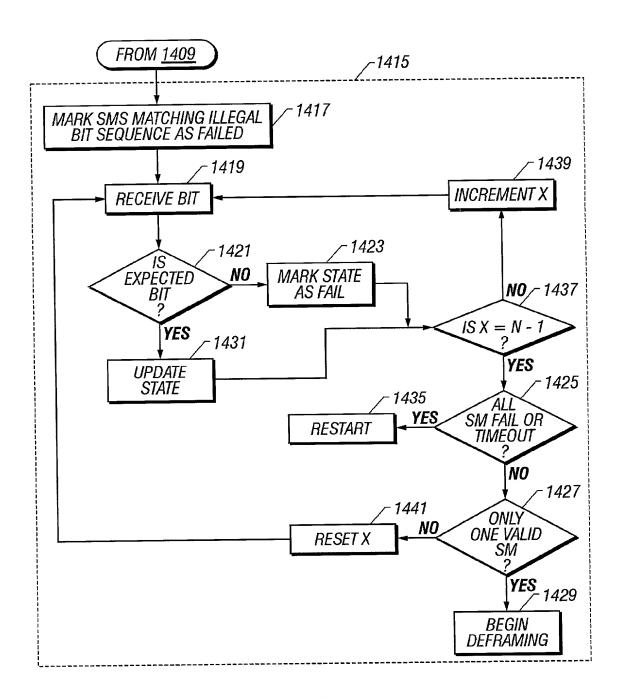


FIG. 14B

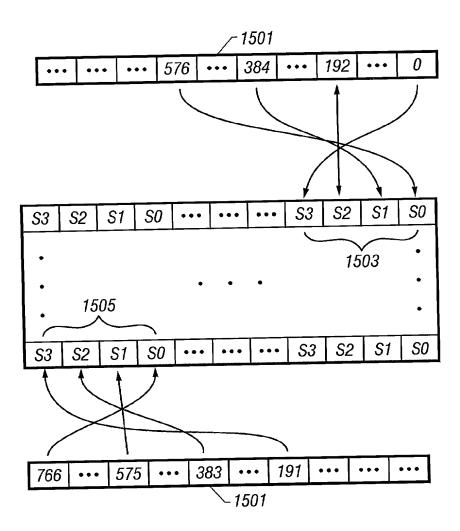


FIG. 15

YES

YES

IS

X = N

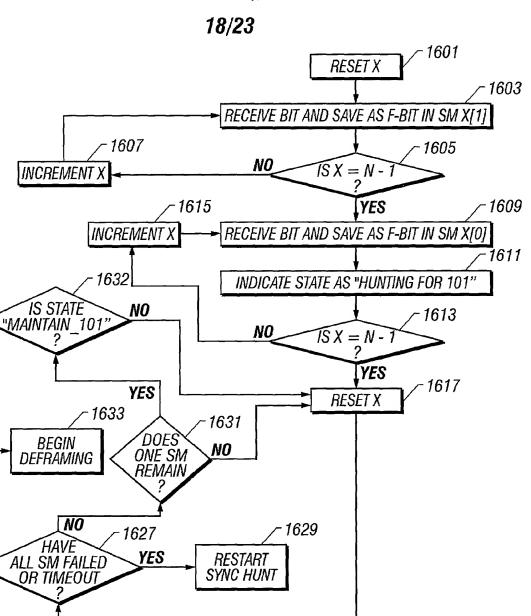
INCREMENT X

1625

NO

1623

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1619

1621

RECEIVE BIT

ALIGNMENT BIT PATTERN VERIFICATION

FIG. 16A

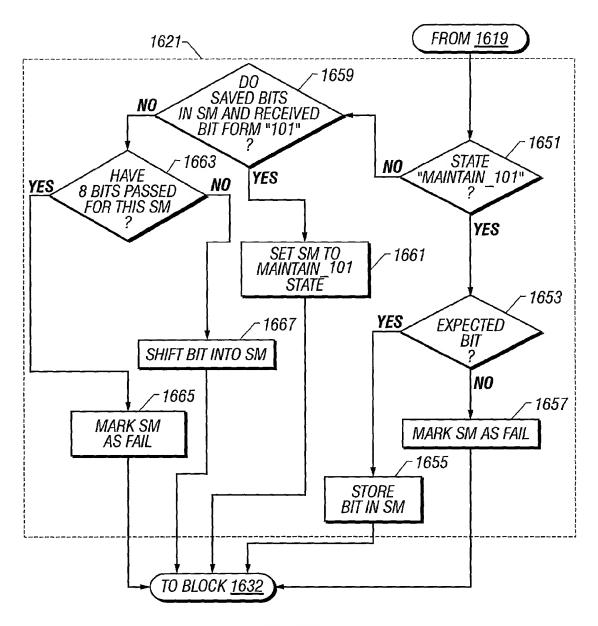
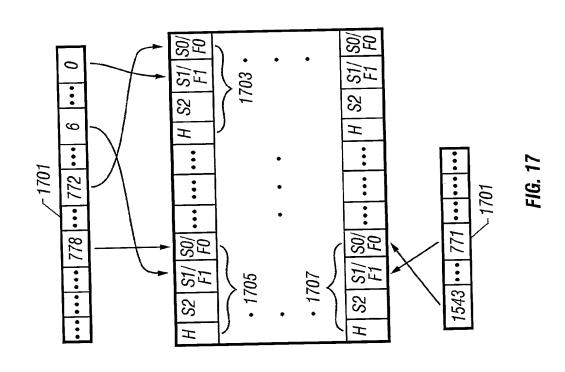


FIG. 16B

					_									
8769	•	•	•	S13	S6	•	•	•	8769	•	•	•	S13	98
89/8				S12	<b>S</b> 5				89/8				S12	S5
29/5				S11	S4				29/5				511	S4
99/8				S10	SS				99/S				S10	S3
29/5				89	22				3765				89	23
S764 S765 S766				88	SI				S764				88	S1
2763	•	•	•	<i>S7</i>	SO	•	•	٠	2763	•	•	•	\$7	80

FIG. 18



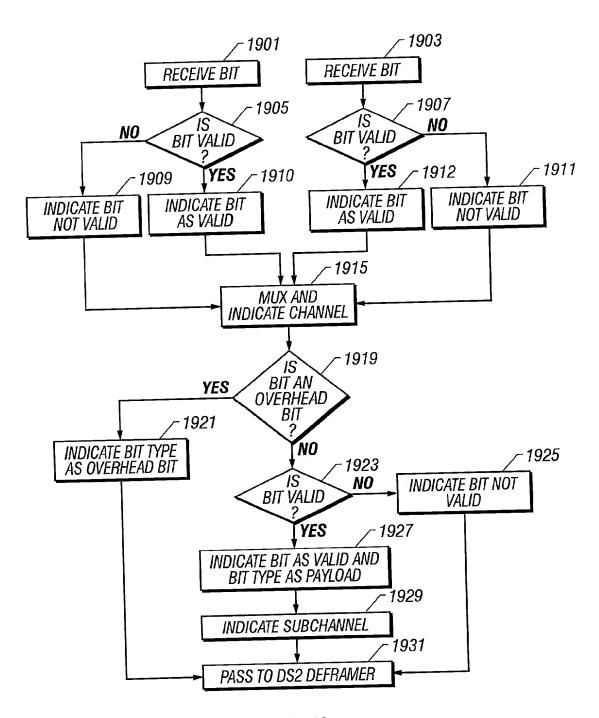


FIG. 19

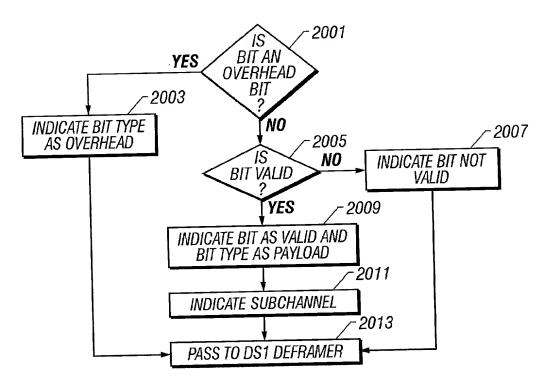


FIG. 20

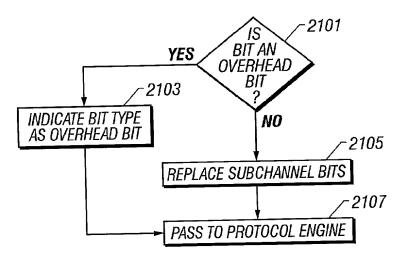


FIG. 21

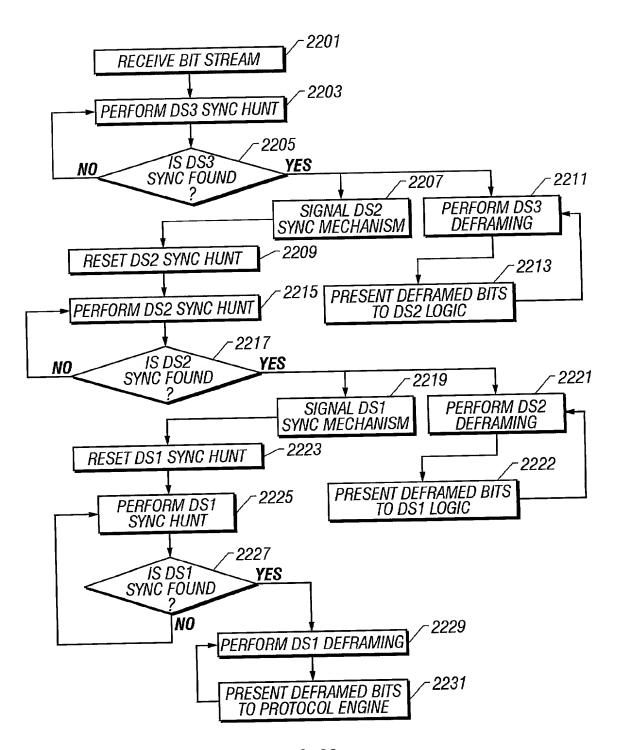


FIG. 22